

I hereby certify that this correspondence is being deposited on this date with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Assistant Commissioner of Patents, Washington, D.C. 20231 on April 23, 2001

Jeanette L. Taplin

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):

Juergen Kockmann

Examiner:

Unassigned

Serial No.:

09/728,879

Group Art Unit:

Unassigned

Filed: Title: December 1, 2000

December 1, 2000

IMPROVEMENT OF BIT ERROR RATE IN A TDMA FREQUENCY

HOPPING SPREAD SPECTRUM SYSTEM BY USING ADDITIONAL

TRANSMIT SLOTS

Assistant Commissioner for Patents Washington, DC 20231

SUBMITTAL OF FORMAL PATENT DRAWINGS

Sir:

In response to the Notice to File Corrected Application Papers, Applicants hereby submit formal drawings consisting of four (4) sheets in the above matter.

Siemens Corporation

Attn: Elsa Keller, Legal Administrator Intellectual Property Department

186 Wood Avenue South

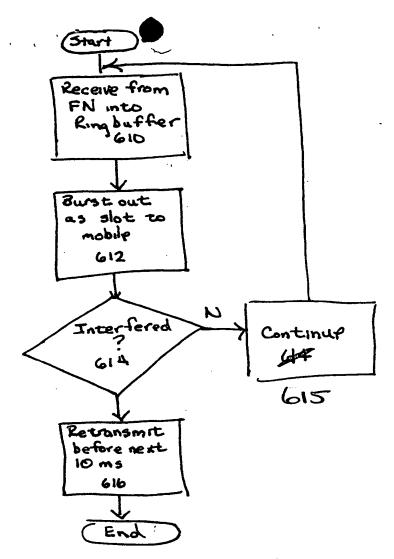
Iselin, NJ 08830

Respectfully submitted,

Tracy L. Hort \'\'
Attorney for Applicant(s)

Reg. No. 34,188

Direct: 408-492-7324



F14.6

STABLE S

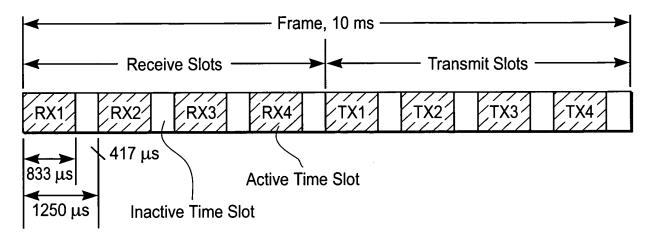
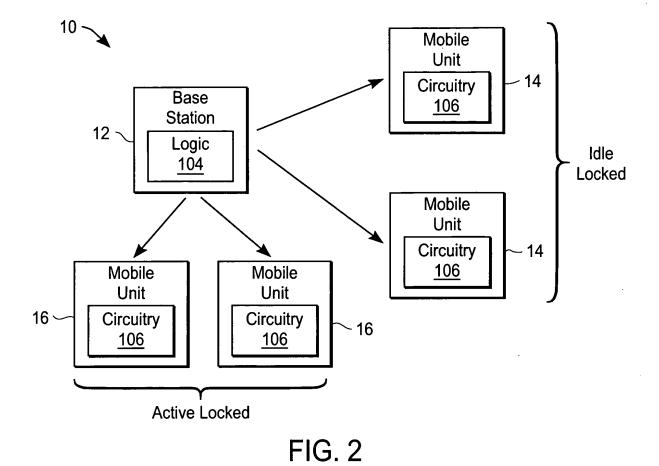


FIG. 1



LOVEBIO "OSEVOL

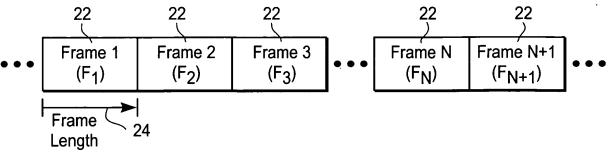


FIG. 3

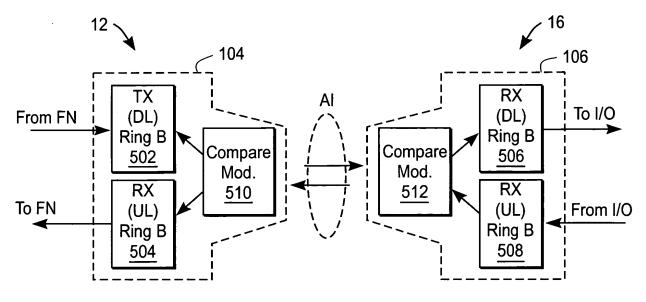


FIG. 4

DOYDEBYO DEEYOL

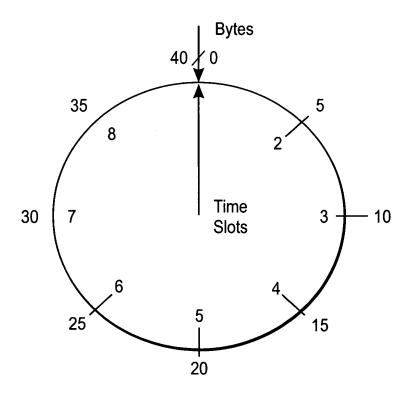


FIG. 5

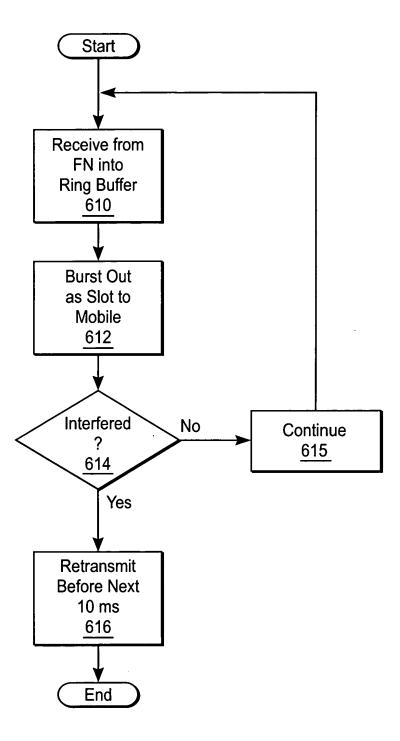


FIG. 6

DOVERDO DEEVED